

JC598 U.S. 4P70
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KODAK

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PATENT NUMBER

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U.S. UTILITY Patent Application

O.I.P.E.		PATENT DATE
KW	SCANNED 	O.A. 

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/536452		712	209	2183 2702	<i>AN</i> <i>Suraa</i>

REPPY, ROBIN
ALEXANDER PELEG

Confining an application to a 32-bit address space subset in a 64-bit processor

PTO-2040
12/09

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed				ISSUE BATCH NUMBER	

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